

❖ *Reading RAM and ROM:* You can't read from ROM in part of the bank-switched memory and read from RAM in the rest: specifically, you can't read the Monitor in ROM while reading bank-switched RAM. If you want to use the Monitor firmware with a program in bank-switched RAM, copy the Monitor from ROM (locations \$F800 through \$FFCB) into bank-switched RAM. You can't do this from Pascal or ProDOS.

To see how to use these switches, look at the following section of an assembly-language program:

AD 83 C0	LDA \$C083	*SELECT 2ND 4K BANK & READ/WRITE
AD 83 C0	LDA \$C083	*BY TWO CONSECUTIVE READS
A9 D0	LDA #\$D0	*SET UP...
85 01	STA BEGIN	*...NEW...
A9 FF	LDA #\$FF	*...MAIN-MEMORY...
85 02	STA END	*...POINTERS...
20 97 C9	JSR RAMTST	*...FOR 12K BANK
AD 8B C0	LDA \$C08B	*SELECT 1ST 4K BANK
20 97 C9	JSR RAMTST	*USE ABOVE POINTERS
AD 83 C0	LDA \$C088	*SELECT 1ST BANK & WRITE PROTECT
A9 80	LDA #\$80	
E6 10	INC TSTNUM	
20 58 C9	JSR WPTSINIT	
AD 80 C0	LDA \$C080	*SELECT 2ND BANK & WRITE PROTECT
E6 10	INC TSTNUM	
A9 01	LDA #PAT12K	
20 58 C9	JSR WPTSINIT	
AD 8B C0	LDA \$C08B	*SELECT 1ST BANK & READ/WRITE
AD 8B C0	LDA \$C08B	*BY TWO CONSECUTIVE READS
E6 0E	INC RWMODE	*FLAG RAM IN READ/WRITE
E6 10	INC TSTNUM	
A9 08	LDA #PAT4K	
20 58 C9	JSR WPTSINIT	

The LDA instruction, which performs a read operation to the specified memory location, is used for setting the soft switches. The unusual sequence of two consecutive LDA instructions performs the two consecutive reads that write-enable this area of RAM; in this case, the data that are read are not used.